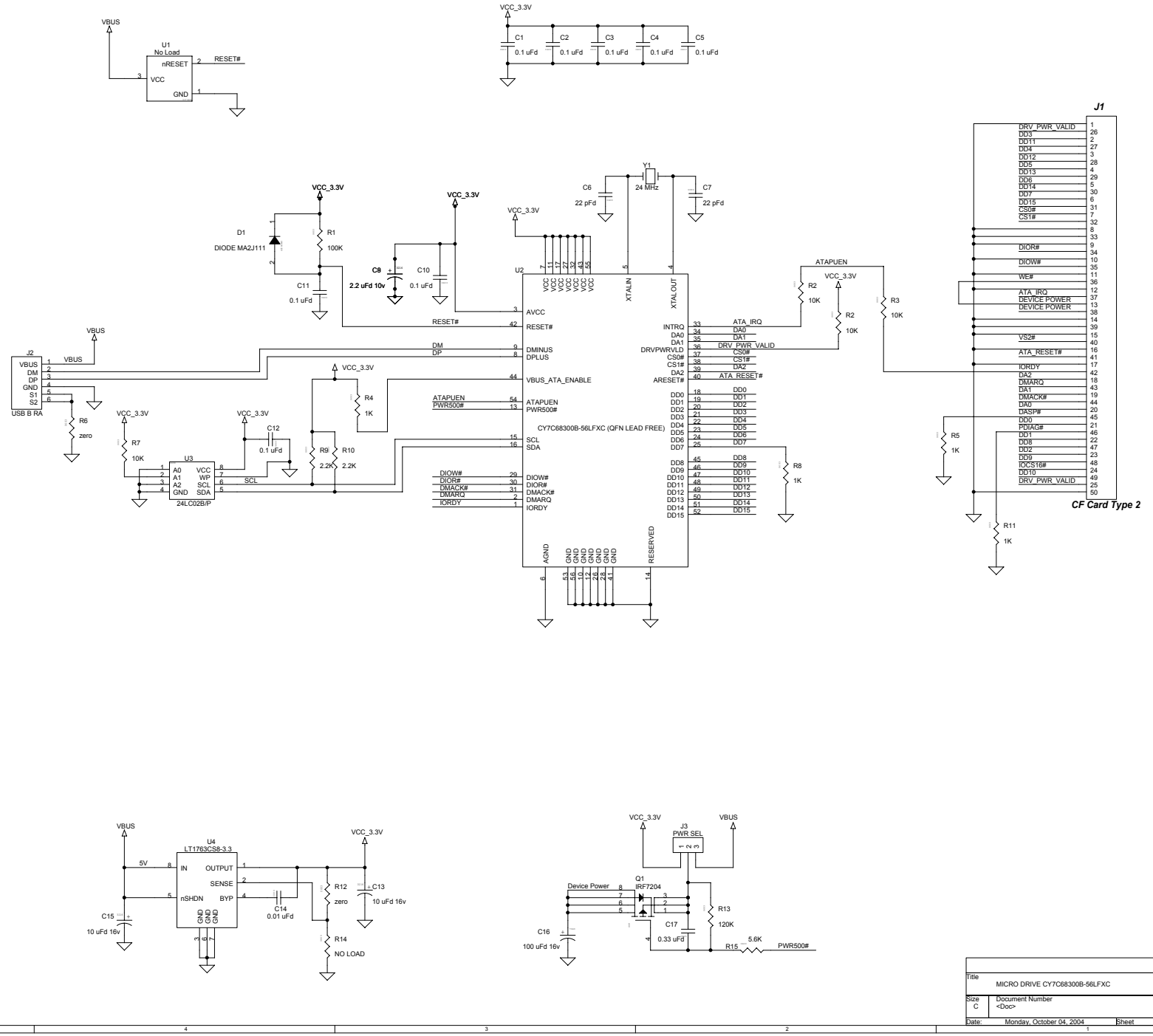

VBUS
U1
No Load
nRESET
VCC
GND
RESET#
VCC_3.3V
C1
C2
C3
C4
C5
0.1 uFd
J1
CF Card Type 2
Y1
24 MHz
C6
C7
22 pFd
D1
DIODE MA2J111
R1
100K
C8
2.2 uFd 10v
C10
C11
0.1 uFd
U2
CY7C68300B-56LFXC (QFN LEAD FREE)
AVCC
RESET#
DMINUS
DPLUS
VBUS_ATA_ENABLE
ATAPUEN
PWR500#
SCL
SDA
DIOW#
DIOR#
DMACK#
DMARQ
IORDY
XTALIN
XTALOUT
RESERVED
AGND
INTRQ
DRVPWRVLD
CS0#
CS1#
DA2
ARESET#
DD0
DD1
DD2
DD3
DD4
DD5
DD6
DD7
DD8
DD9
DD10
DD11
DD12
DD13
DD14
DD15
ATA_IRQ
DA0
DA1
DRV_PWR_VALID
DAZ
ATA_RESET#
R2
10K
R3
10K
R4
1K
R5
1K
R6
zero
R7
10K
R8
1K
R9
2.2K
R10
2.2K
R11
1K
J2
USB B RA
DM
DP
GND
S1
S2
U3
24LC02B/P
A0
A1
A2
GND
WP
SCL
SDA
C12
0.1 uFd
U4
LT1763CS8-3.3
5V
IN
OUTPUT
SENSE
nSHDN
BYP
C13
10 uFd 16v
C14
0.01 uFd
C15
10 uFd 16v
R12
zero
R14
NO LOAD
J3
PWR SEL
Device Power
Q1
IRF7204
C16
100 uFd 16v
C17
0.33 uFd
R13
120K
R15
5.6K
PWR500#
Title
MICRO DRIVE CY7C68300B-56LFXC
Size
C
Document Number
<Doc>
Rev
<RevCode>
Date:
Monday, October 04, 2004
Sheet
1
of
1